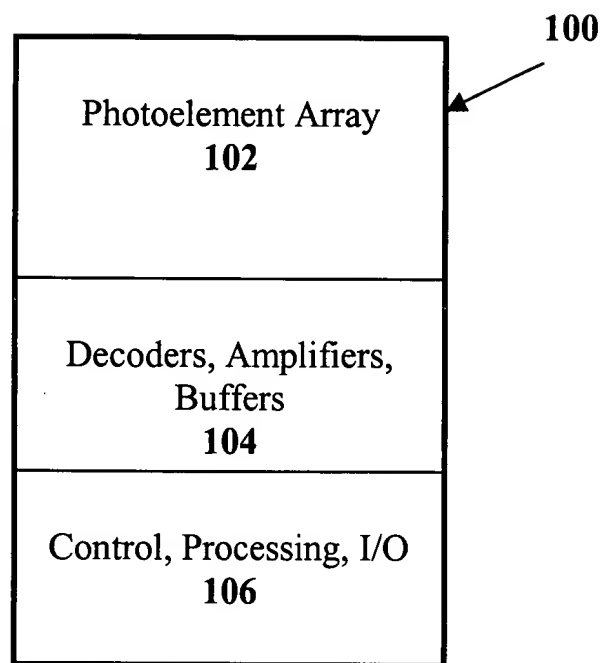


FIG. 1 is a block diagram of a prior art system.



**Figure 1**  
(prior art)



Figure 3A

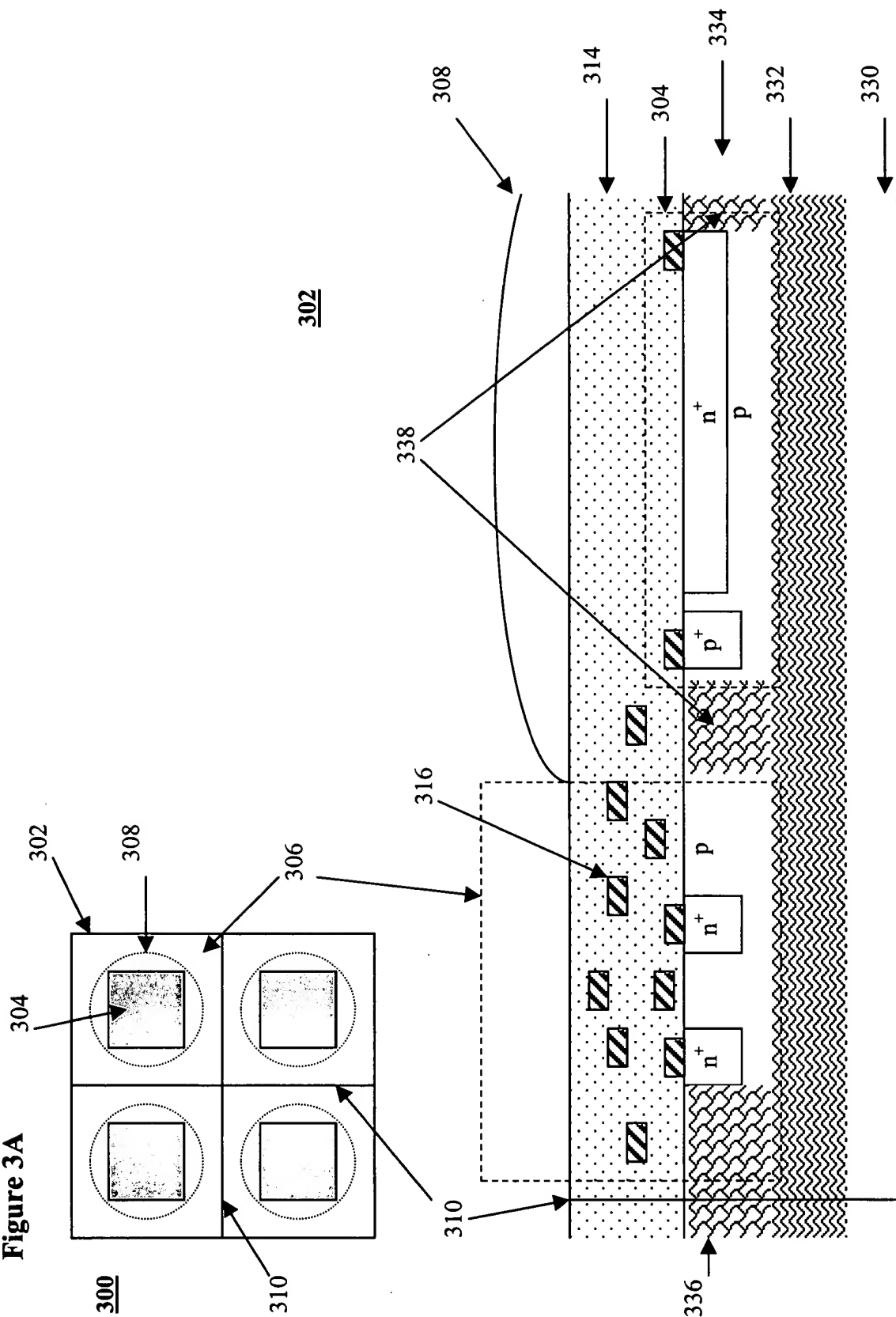


Figure 3B

Figure 4A

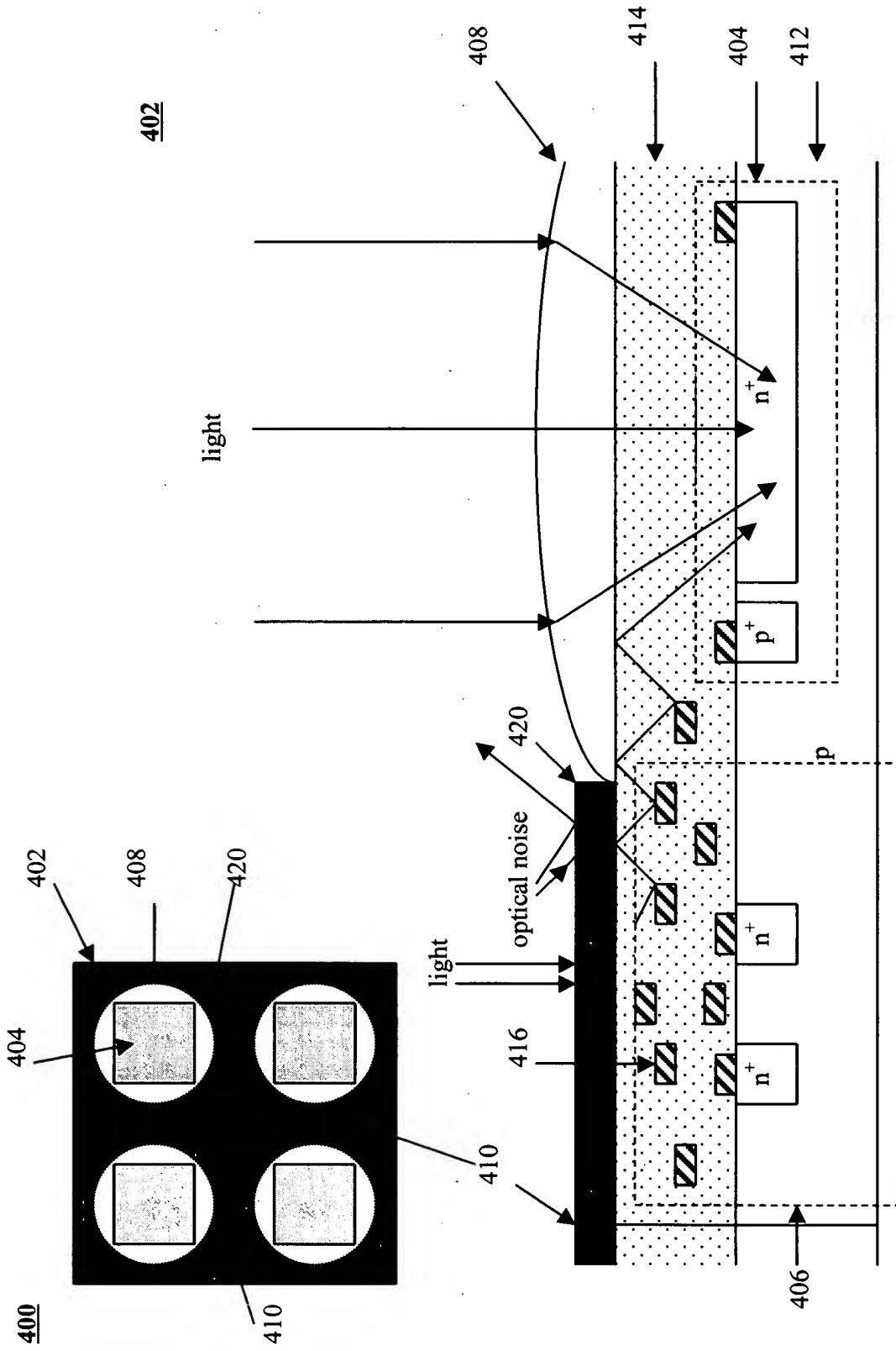
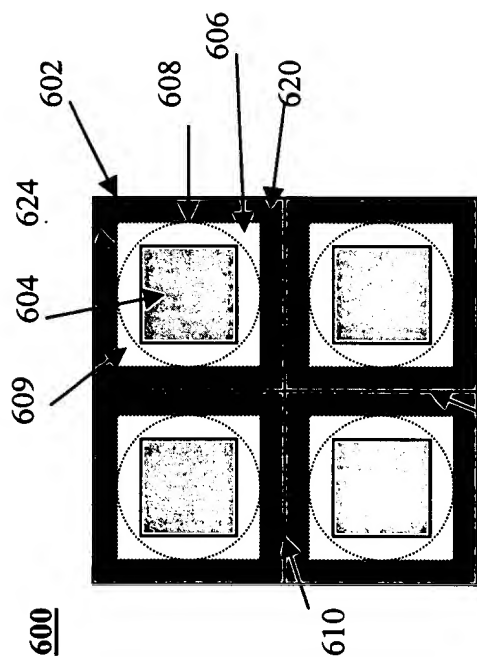


Figure 4B



### Figure 6A



**Figure 6B**

